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TECHNOLOGY CENTER 2800

Docket No.: 40532-P001M11P3

Commissioner For Patents
Washington, D. C. 20231

Inventor(s): Peter D. Geiger, Manual J. Alvarez II, Thomas A. Dye
Title: System And Method For Generating Optimally Compressed Data From A
Plurality Of Data Compression / Decompression Engines Implementing
Different Data Compression Algorithms
Serial No.: 10/044,785 Filing Date: 1/11/02
Examiner: Howard L. Williams Group Art Unit: 2819
Winstead Docket No.: 40532-P001M11P3 (f/k/a 5143-03100)

Transmitted herewith are the following:

1. Return Receipt Postcard;
2. This Transmittal Letter (in duplicate);
3. 2 pages of Information Disclosure Statement Under 37 CFR § 1.97(c) With Fee (in duplicate);
4. 4 pages of PTO Form 1449;
5. Copies of cited references in PTO Form 1449 (A1-A97; B1-B4; C1-C3); and
6. Firm check in the amount of \$180.00.

I have enclosed a firm check in the amount of \$180.00. If the fee has been calculated incorrectly, the Commissioner is hereby authorized to charge any insufficiency of payment of the following fees associated with this communication, or credit any overpayment, to Deposit Account No. 23-2426 (40532-P001M11P3) in the name of Winstead Sechrest & Minick P.C. A **duplicate copy of this transmittal letter is enclosed.**



CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence (along with any item referred to as being enclosed herewith) is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on April 21, 2003.

Michael Adams

Signature

Respectfully submitted,

Michael Adams

Michael P. Adams
Attorney for Applicant(s)
Reg. No. 34,763

MPA:wk
Enclosure(s)

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40532-P001M11P3 04/21/2003



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 25 2003
TECHNOLOGY CENTER 2800

Applicant(s): Peter D. Geiger; Manuel J. Alvarez II; Thomas A. Dye
Assignee: Interactive Silicon, Inc.
Title: System And Method For Generating Optimally Compressed Data From
A Plurality Of Data Compression / Decompression Engines
Implementing Different Data Compression Algorithms
Serial No.: 10/044,785 Filing Date: 1/11/02
Examiner: Howard L. Williams Group Art Unit: 2819
Docket No.: 40532-P001M11P3
(f/k/a 5143-03100)

Dallas, Texas
April 21, 2003

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(c) WITH FEE

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

These documents include documents that were cited in patent(s) and/or patent application(s) related to the present patent application.

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I have enclosed a firm check in the amount of \$180.00. If the fee has been calculated incorrectly, the Commissioner is hereby authorized to charge any insufficiency of payment of the following fees associated with this communication, or credit any overpayment, to Deposit Account No. 23-2426 (P001M11P3) in the name of Winstead Sechrest & Minick P.C. A duplicate copy of this transmittal letter is enclosed.



Michael P. Adams
Attorney for Applicant(s)
Reg. No. 34,763

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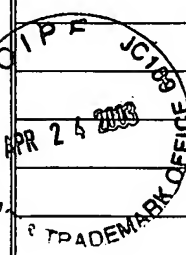
Signature

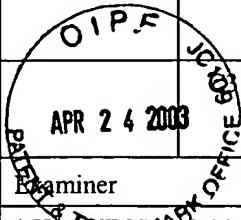
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40532-P001M11P3 04/21/2003

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					40532-P001M11P3		10/044,785	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Peter D. Geiger, et al.			
					Filing Date		Group	
					1/11/02		2819	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	A1	5,640,529	6/97	Hasbun				
	A2	5,568,423	10/96	Jou et al.				
	A3	5,559,978	9/96	Spilo				
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	A5	5,504,842	4/96	Gentile				
	A6	5,465,338	11/95	Clay				
	A7	5,479,638	12/95	Assar				
	A8	5,477,264	12/95	Sarbadhikari et al.				
	A9	5,459,850	10/95	Clay et al.				
	A10	5,448,577	9/95	Wells et al.				
	A11	5,357,614	10/94	Pattisam et al.				
	A12	5,341,339	8/94	Wells				
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	A14	5,247,638	9/93	O'Brien et al.				
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	A16	5,696,912	12/97	Bicevskis et al.				
	A17	5,584,008	12/96	Shimada et al.				
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	A23	5,563,595	10/8/96	Strohacker				
	A24	5,455,577	10/95	Slivka et al.				
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	A31	4,881,075	11/89	Weng			
	A32	5,379,036	1/3/95	Storer			
	A33	4,876,541	10/24/89	Storer			
	A34	5,973,630	10/26/99	Heath			
	A35	5,956,372	9/21/99	Vaman et al.			
	A36	5,729,228	3/17/98	Franaszek et al.			
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	A39	5,126,739	6/30/92	Whiting et al.			
	A40	5,003,307	3/26/91	Whiting et al.			
	A41	5,016,009	5/14/91	Whiting et al.			
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	A48	5,936,560	8/10/99	Higuchi			
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	A62	5,406,279	4/11/95	Anderson et al.			
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	A67	5,150,430	9/22/92	Chu			
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	A75	6,002,411	12/14/99	Dye				
	A76	5,394,534	1/95	Kulakowski				
	A77	6,304,197 B1	10/16/01	Freking et al.				
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A80	5,424,733	6/13/95	Fimoff et al.					
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A94	60/307,964	7/26/01	Dye et al.					
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A96	10/205,590	7/25/02	Dye et al.					
A97	10/044,786	1/11/02	Dye et al.					
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	B1	0 702 457	3/96	EP				
	B2	95/19662	7/95	WO				
	B3	92/17844	10/15/92	WO				
	B4	95/18997	7/13/95	WO				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	C1	Brenza, "Synonym Avoidance Cache," IBM Technical Disclosure Bulletin, Vol. 34, No. 1, June 1991, pp. 377-381.						

	C2	Y. Yabe et al.; "Compression/Decompression DRAM for United Memory Systems: a 16Mb, 200MHz, 90% to 50% Graphics-Bandwidth Reduction Prototype"; Solid-State Circuits Conf., 1998; Digest of Technical Papers; 1998 IEEE Int'l; San Francisco, CA, USA; 5-7 Feb. 1998; NY, NY, USA; IEEE; US; 5 February, 1998 (1998-02-05); pp. 342-343; XP010278670; ISBN 0-7803-4344-1.
		M. Kjelso et al.; "Performance evaluation of computer architectures with main memory data compression"; Journal of Systems Architecture; Elsevier Science Publishers Bv.; Amsterdam, NL; Vol. 45, No. 8; February 1999 (1999-02); pgs. 571-590; XO004153056; ISSN: 1383-7621; pg. 574, left-hand column, paragraph 4; pg. 580, left-hand column, paragraph 2.
Examiner	Date Considered	
*EXAMINER Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

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40532-P001M11P3 04/18/2003